ABSTRACT OF THE DISCLOSURE

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A coding parameter obtained by a first coding processing is transferred from signal processing sections (403) to (406) to a parameter input/output section (408) through a coding control section (407), and the parameter input/output section (408) stores the coding parameter in an external DRAM (411) through an SDRAM interface section (410). In a second coding processing, the coding parameter stored in the external DRAM (411) is transferred to the parameter input/output section (408) through the external DRAM (411), and the parameter input/output section (408) gives the acquired coding parameter to the signal processing sections (403) to (406) through the coding control section (407).